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MORRISON FOERSTER

755 PAGE MILL ROAD
PALO ALTO
CALIFORNIA 94304-1018

TELEPHONE: 650.813.5600 FACSIMILE: 650.494.0792

WWW.MOFO.COM

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FACSIMILE:	TELEPHONE: (703) 308-1202	
(703) 746-9195		
DAT	E: June 27, 2005	
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Comments:

Attorney Docket No.: 333772000800

Number of pages with cover page:

Group Art Unit: 2829

Examiner: Not Yet Assigned Application No. 10/772,434 Filed: February 6, 2004

Inventor(s): Ramachandran KRISHNASWAMY et al.

Title: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR

SEMICONDUCTOR INTEGRATED CIRCUITS

Papers Attached:

- 1. Transmittal Form (1 page)
- 2. Second Request For Corrected Filing Receipt (2 pages)
- 3. Copy of Request for Corrected Filing Receipt filed June 10, 2005 (2 pages)
- 4. Copy of Foreign Filing License with a Grant Date of February 6, 2004 (1 page)

Art Unit 2829 Examiner Name Not Yet Assigned Total Number of Pages In This Submission 6 Attorney Docket Number 333772000800 ENCLOSURES (Check all that apply) ENCLOSURES (Check all that apply) Fee Transmittal Form Drawing(s) After Allowance Communication to TC Appeal Communication to Boar Appeals and Interferences Amendment/Reply Petition to Convert to a Provisional Application After Final Provisional Application Affidavits/declaration(s) Provisional Application Extension of Time Request Terminal Disclaimer Express Abandonment Request Request Request For Corneded Fing Receipt Rel	dan Ber Wanananada D	teduction Act of 1995, no nece	lees of bariupes ene and	U.S. Petent and pond to a collection	1 Tandows web	of for use through 07/31/2006, OMB 0851-003 Office; U.S. DEPARTMENT OF COMMERC On unless it displays a valid OMB control number
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Dated: June 27, 2005

Slonsture

(Kerry/C, Keehan)

Docket No.: 333772000800

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ramachandran KRISHNASWAMY et al.

Application No.: 10/772,434

Filed: February 6, 2004

Art Unit: 2829

For: METHOD AND STRUCTURE TO DEVELOP

A TEST PROGRAM FOR SEMICONDUCTOR

INTEGRATED CIRCUITS

Examiner: Not Yet Assigned

Confirmation No.: 5995

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in

If Required, Foreign Filing License Granted: delete "05/01/2004" and insert -- 02/06/2004-- in place thereof.

Copies of the first Request For Corrected Filing Receipt requesting the change, and License For Foreign Filing are attached.

Application No.: 10/772,434

2

Docket No.: 333772000800

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: June 27, 2005

Respectfully submitted,

Thomas Chan

Registration No.: 51,543

MORRISON & FOERSTER LLP

755 Page Mill Road

Palo Alto, California 94304-1018

(650) 813-5616

2005

JUN 27 2005

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Docket No.: 333772000800

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Ramachandran KRISHNASWAMY et al.

Confirmation No.: 5995

Application No.: 10/772,434

Art Unit: 2829

Filed: February 6, 2004

For: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR SEMICONDUCTOR

Examiner: Not Yet Assigned

INTEGRATED CIRCUITS

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections Office of Initial Patent Examination Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the aboveidentified patent application. The official Filing Receipt received by Applicant, a marked-up copy of which is attached hereto, has the following errors, as indicated on the marked-up copy.

Under the heading "Assignment for Published Patent Application", please replace "Santa Clara, CA" with -Tokyo, Japan --. A copy of the Notice of Recordation of Assignment is attached hereto.

Under the heading "If Required, Foreign Filing License Granted:" please replace "05/05/2004" with --02/06/2004-. A copy of the Foreign Filing License is attached hereto.

sf-1843505

Application No.: 10/772,434

2

Docket No.: 333772000800

The mistake occurred in the printing of the Filing Receipt, therefore it is not believed that any fee is required. However, if this is not the case, the Assistant Commissioner is hereby authorized to charge the required fee to <u>Deposit Account No. 03-1952</u>. Acceptance of this Request for Corrected Filing Receipt is respectfully requested.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

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Dated: 6/9/0

Respectfully submitted,

Robert A. Saltzberg

Registration No.: 36,910

MORRISON & FOERSTER LLP

425 Market Street

San Francisco, California 94105

(415) 268-6428

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FIRST NAMED APPLICANT

ATTORNEY DOCKET NO.

P-108,530

2/6/04

RAMACHANDRAN KRISHNASWAMY

333772000800

Paper Number

Title:

METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR SEMICONDUCTOR INTEGRATED CIRCUITS

Art Unit

Correspondence Address:

ROBERT A. SALTZBERG MORRISON & FOERSTER LLP. 425 MARKET STREET SAN FRANCISCO, CA. 94105

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[Title 35, United States Code (1952) Sections 184, 185, 186]

